

# PFP-ZU+ DK

ZYNQ ULTRASCALE+ BOARD DEVELOPMENT KIT



## Easy implementation of Zynq architecture



### BENEFITS

- Quick and complete handling of Zynq
- Easy Zynq implementation
- PFP-ZU+ I/Os supported
- Xilinx tools supported :
  - VIVADO
  - Vitis
  - etc.
- Permanent upgrade :
  - Latest tool versions
  - Drivers
- Windows & Linux

### FIRMWARE

- High-performance PCIe driver :
  - Continuous buffer
  - DMA
  - etc.
- Dedicated and optimized DDR4 RAM IP

### SOFTWARE

- For all PS interfaces :
  - Vitis (bare metal) examples
  - PetaLinux examples
- Multiple boot modes

The PFP-ZU+ Development Kit has been developed to allow users, both novices and experts in Zynq technology, to easily implement the unique features of the Zynq architecture.

TECHWAY offers a solution to minimize the risks of using a new hybrid FPGA/CPU technology. The DK allows our customers to discover and benchmark without mobilizing important expert resources.

The Development Kit remains a decisive element allowing to focus the effort on the application developments rather than on the low level layers (FPGA/CPU communication, PCIe driver, etc.).

Its standard API is compliant with the TECHWAY's PFP (PCIe FPGA Platforms) product line. This allows quick and easy portability and limited redesign of applications to future PFP boards, for a long-lasting investment.

The Development Kit provides examples of how to use the interfaces and multiple boot modes of the board. It allows FPGA and Software developers to work independently from each other.

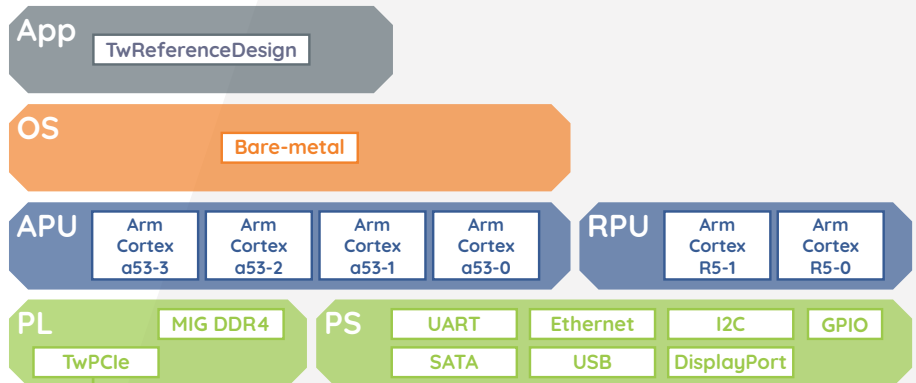
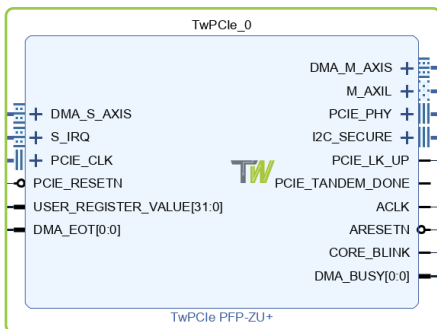


DEFENCE



INDUSTRY

### PFP-ZU+ TECHWAY PCIe IP



### Vitis Reference Design overview

Information and photos subject to change without notice

