



OPEN FOR DEVELOPMENT

TECHWAY
SIGNAL VISION SYSTEM

PCIe FPGA/SoC board

PFP Products

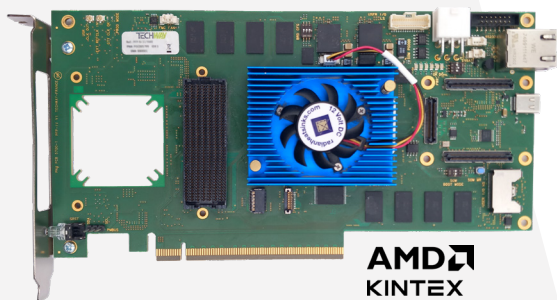
FMC+ Connector

Stand-alone

I/O connector

ITAR Free

PFP
IV



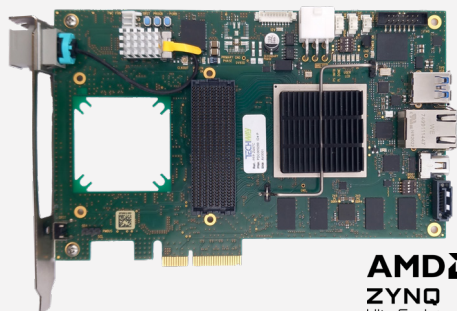
AMD
KINTEX
UltraScale+

High-speed
applications
up to 25 Gbps

Kintex UltraScale+ FPGA

- Kintex UltraScale+ FPGA
XCKU11P, XCKU15P
- PCIe x16 Gen3
- Zynq UltraScale+ MPSoC SoM (option)
- 4x optical links (Firefly™ option)
- Up to 25Gbps per HSS link
- 2x 4GB DDR4 memory banks

PFP
ZU+



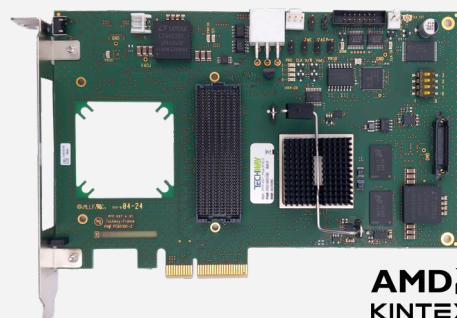
AMD
ZYNQ
UltraScale+

Processing on
board
(CPU+FPGA)

Zynq UltraScale+ SoC

- Zynq UltraScale+ MPSoC
ZU7CG, ZU7EV, ZU11EG
- PCIe x4 Gen3
- 4x optical links (Firefly™ option)
- Up to 16,3 Gbps per HSS link
- 2x 1GB DDR4 memory banks

PFP
KX7+



AMD
KINTEX7

Cost-effective
solution

Kintex-7 FPGA

- Kintex-7 FPGA
XC7K325T, XC7K410T
- PCIe x4 Gen2
- Up to 10 Gbps rate per HSS link
- 2x 512MB DDR3 memory banks

Development Kit included

100% in house design

Integrate your own application

Windows and Linux

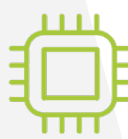


Software



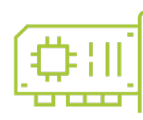
API C++
Examples
Drivers

Firmware



Ready-to-use IPs
(PCIe, Memories, System management)
Examples

Hardware



User manuals
Reliable 24/7
Long-life cycle

AMD
XILINX
Certified Partner





FMC Products

FPGA Mezzanine Card

VITA 57 compliant

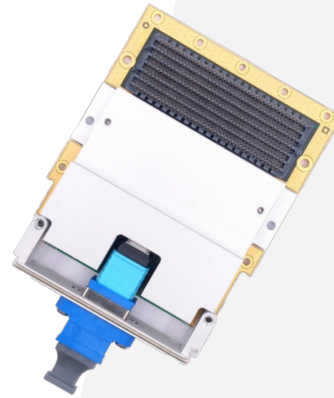
Any FPGA carrier

Optical enabler

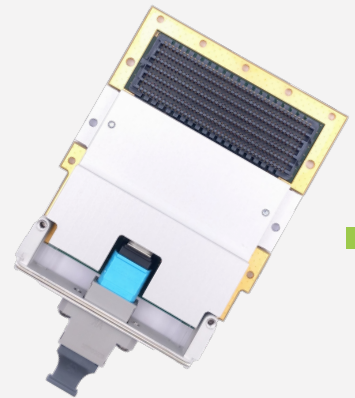
Industrial or rugged

TigerFMC - optical FMC up to 25 Gbps

- VITA 57.4 compliant
- SAMTEC Firefly™ optical interface
- Up to 12 full duplex links
- Up to 25 Gbps per link
- Front connection : MTP or rugged
- 4x programmable oscillators
- Standard / Extended temperature



TigerFMC-4_1



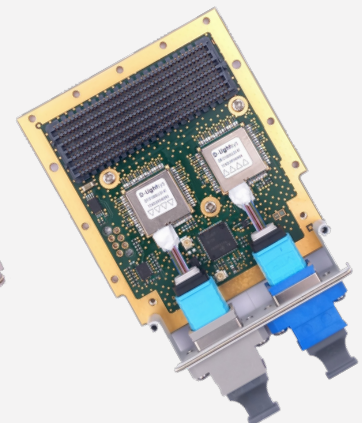
TigerFMC-12_1

WildcatFMC - optical FMC up to 10 Gbps

- VITA 57.4 compliant
- RADIAL D-Lightsys® optical interface
- Up to 12 full duplex links
- Up to 10 Gbps per link
- Front connection : MTP or rugged (C-MTITAN)
- Rear connection on demand
- 4x programmable oscillators
- Air / conduction cooled



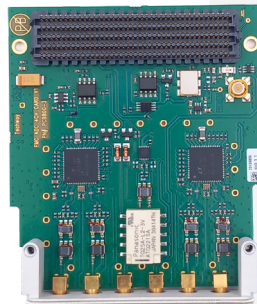
WildcatFMC-4_1 C-MTITAN



WildcatFMC-12_1 MTP

ADC125 Mezzanine

- VITA 57.1 compliant
- 4x 16bits A/D channels
- Up to 125 MSPS
- On-board sampling clock



ADC125 FMC

SFP / SFP + Mezzanine

- VITA 57.1
- 3 or 4x SFP/SFP+ slots
- 1x programmable oscillator
- Up to 10 Gbps
- Stackable version



SFP/SFP+ FMC

Any applications

Any protocols

Ethernet (1/10/100 Gbps), ARINC 818, sFPDP, Fiber Channel, Xilinx Aurora, User-defined and more.