

APPLICATION NOTE

EAGLE_16-125-VTG

AN2100



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Review tracker

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1.0	11/21/2016	Creation	BMA
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1 APPLICABLE DOCUMENTS

1.1 **TECHWAY documents**

Table 1: TECHWAY documents

Ref.	Title	Origin	Document ID
[T1]	EAGLE_16-125 user manual	TECHWAY	UM2100
[T2]	EAGLE_16-125 narrow band processing user manual	TECHWAY	UM2140
[T3]	EAGLE_16-125 narrow band user manual of WEB interface	TECHWAY	UM2110
[T4]	EAGLE_16-125 narrow band user manual of SNMP agent	TECHWAY	UM2120
[T5]	EAGLE_16-125 user manual of TEA API	TECHWAY	UM2130
[T6]	Document list	TECHWAY	UM2199







2 WARNING

INFORMATION AND FIGURES SUBJECT TO CHANGE WITHOUT PRIOR NOTICE.

USER ASSUMES ENTIRE RISK FOR THE USE OF THE PRODUCT.

TECHWAY SHALL NOT BE HELD LIABLE FOR ANY SYSTEM DAMAGE, DATA LOSS OR OTHER DAMAGES RESULTING FROM THE USE OR MISUSE OF THE PRODUCT.

3 SCOPE OF DOCUMENT

EAGLE_16-125 allows users to tune its DDC to fit a wide range of applications.

Details on EAGLE_16-125's narrow band processing can be found in EAGLE_16-125's narrow band processing user manual [T1].

When configured with narrow band baseline **BL_EAGLE_VERTIGO_FWx_SWy**, EAGLE_16-125's DDC looks like in *Figure 1*.



Figure 1: EAGLE_16-125 narrow band DDC

Here are the parameters which are customizable:

- DDS's central frequency
- CIC filter's decimation
- cFIR filter's frequency response
- pFIR filter's frequency response

This application note presents the filters' different roles and how to configure them to fit a specific need.







4 DESIGNING EAGLE_16-125 NARROW BAND DDC'S PROCESSING

4.1 DDC filtering specifications

Through this application note, a filter will be designed. This filter will have the following specifications:

- Sample frequency (f_s): 100 MHz
- Decimation: 420
- Pass-band ripple: 0.1 dB
- Cutoff frequency: 86 kHz
- Stop-band frequency: 120 kHz
- Stop-band attenuation: 100 dB





4.1.1 First stage: CIC filter

4.1.1.1 CIC filter design

The CIC filter is the first part of the filtering stage of the DDC. Its role is to provide a low-pass-high-decimated frequency response.

The EAGLE_16-125 CIC filter has the following fixed parameters:

- Number of stages: 5
- Differential delay: 1

The only parameter that can be configured is the decimation, which can go from 8 to 256.

In our example, as decimation factor of 420 is required, and cFIR and pFIR filters both have a decimation of 2, then the CIC filter has to be configured with a decimation factor of 105.









With these parameters, the CIC filter has the following frequency response:

Figure 3: Gain Bode diagram of CIC filter

Although the CIC filter brings a high decimation and a low-pass frequency response, it has major drawbacks:

- First, the stop band ripple, which is above the -100 dB stop-band specification, will bring frequency aliases into the pass-band of the final filter if left as it is (*Figure 3*).
- The second drawback is the pass-band droop. As the CIC has not a flat pass-band, the droop will degrade the pass-band of the final filter (*Figure 4*).

To compensate these two points, the CIC filter is followed by a compensation filter: the cFIR filter.











4.1.1.2 Decimation configuration

User only has to configure the desired total decimation of the DDC and the EAGLE_16-125 will apply itself the correct decimation on the CIC filter. Configuration can be made either through the EAGLE_16-125 WEB interface (*Figure 5*) or via an SNMP command (*Figure 6*).

	Channel linked Channel 0	Central Frequency 69.95 MHz	Decimation 420	Peak Rate 1.905 MB/s	Data rate 0.08 MB/s
	Input Ch	annel selection	Output Dest Interface 0 ; Configure net	tination selection	
		Filtering s	election ed Y		
	Offset 2	Input Win O samples	dowing tart Trigger event	Period	_
	Width <u>?</u> Period <u>?</u>	4200000 samples	Offset		
Manage PU#0	Set infinite period	Ø		Width	→t
	DDS Cer 69.95	MHz × 2	Filtering D	DC Decimation	

Figure 5: DDC decimation configuration from the WEB interface

echo "DDC decimation setting" IP=192.168.0.253 pu_num=0 decimation=420 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.\${pu_num}.7.0 i \${decimation}

Figure 6: SNMP command for DDC decimation setting

Note:

It is important to configure the decimation factor prior to configure windowing **width**. Decimation factor value is used by EAGLE_16-125 to calculate the real windowing width, which includes DDC filter's rise time, which depends on the decimation factor.

4.1.2 Second stage: cFIR filter

4.1.2.1 cFIR filter design

The cFIR filter's (for compensation Finite Impulse Response filter) main role is to compensate the passband droop of the CIC filter. It narrows the bandwidth and increases the overall attenuation within transition band and stop-band regions of the final filter as well.







It is implemented as a decimate-by-2 26-taps symmetric FIR filter. Transition band roll-off of the cFIR filter does not have to be really steep, as this task will be handled by the pFIR filter. Meanwhile, it is essential that the cFIR filter's stop band starts before half of the CIC filter's sampling frequency so as to avoid frequency aliasing.

In our case, the CIC filter's output sampling frequency is: f_s/CIC decimation factor => 100e6/105 = 952 380 Hz

Then, cFIR filter's stop band shall starts before half of that value: 476 190 Hz. A good practice is to choose quarter of CIC filter's output sampling frequency, so (f_s/CIC decimation factor)/4.

In order to obtain a final pass-band as flat as possible, care must be taken to create a cFIR filter with a minimum pass-band ripple.

Here are the chosen parameters for this example cFIR:

- Cutoff frequency: $(f_s/105)/4 = 238$ kHz
- Pass-band ripple: 0.001 dB
- Stop-band attenuation: 100 dB

With these parameters, the cFIR has the following frequency response:



Figure 7: Gain Bode diagram of cFIR filter

The frequency response of the cFIR filter is determined by a set of coefficients which are used by filter's multipliers. Coefficient set must be provided by user. Some tools like Matlab© can help you to determine or to generate a set of coefficient by giving them the parameters determined above.

If we take a close look at the droop compensation (*Figure 8*), we can see that the pass-band is correctly flattened by the cFIR.













Figure 9: Frequency response of CIC & cFIR combination

Taking a look at the resulting frequency response of the combination of the CIC and cFIR filters (*Figure* 9), we can verify that the cFIR filter attenuates the CIC stop band ripple lobes, and narrows the passband of the final filter.

Still, the three remaining lobes above -100 dB will create aliases within final filter bandwidth. That point will have to be corrected by the next and final filtering stage: the pFIR filter.







4.1.2.2 cFIR configuration

Frequency response of cFIR filter must be adjusted by loading a custom coefficient set. This set can be loaded either through the WEB interface (*Figure 10*) or by an SNMP command (*Figure 11*). Coefficient sets have to be provided respecting Xilinx filter coefficient format with file extension ".coe" (see the *APPENDIX* section at the end of this document).



Figure 10: cFIR's coefficient set loading through the WEB interface

echo "cFIR coefficient loading" IP=192.168.0.253 pu_num=0 coefile=\$(cat ../EAGLE_fs_100_MHz_Decim_420_fc_86_kHz_cFIR.coe) snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.\${pu_num}.8.0 s "\$coefile"

Figure 11: SNMP commands for cFIR coefficient set loading

4.1.3 Third stage: pFIR filter

4.1.3.1 pFIR filter design

The role of the pFIR filter is to realize a filtering to obtain the requested pass-band cutoff and stop-band attenuation. It is implemented as a decimate-by-2 63-taps symmetric FIR filter, which allows implementation of filters with pretty steep transition band roll-off.

Here are the chosen parameters for this example pFIR:

- Cutoff frequency: 89.5 kHz
- Pass-band ripple: 0.1 dB
- Stop band attenuation: 100 dB







With these parameters, the pFIR has the following frequency response:



Figure 12: pFIR filter gain Bode diagram

The frequency response of the pFIR filter is determined by a set of coefficients which are used by filter's multipliers. Coefficient set must be provided by user. Some tools like Matlab© can help you to determine or to generate a set of coefficient by giving them the parameters determined above.

A closer look at the pass-band ripple of the pFIR (*Figure 13*) indicates, with the previous flat pass-band of CIC and cFIR combination, that the 0.1 dB ripple specification of the final filter will be fulfilled.



Figure 13: pFIR pass-band ripple



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4.1.3.2 pFIR configuration

pFIR filter's frequency response can be adjusted by loading a custom coefficient set. This set can be loaded either through the WEB interface (in the exact same way than with the cFIR, see *Figure 10*) or by an SNMP command (*Figure 14*). Coefficient sets have to be provided respecting Xilinx filter coefficient format with file extension ".coe" (see the *APPENDIX* section at the end of this document).

echo "pFIR coefficient loading" IP=192.168.0.253 pu_num=0 coefile=\$(cat ../EAGLE_fs_100_MHz_Decim_420_fc_86_kHz_pFIR.coe) snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.\${pu_num}.9.0 s "\$coefile"

Figure 14: SNMP commands example for pFIR coefficient file loading







5 COMPLETE DDC FILTER

When combining CIC, cFIR & pFIR filters, the following gain bode diagram is obtained:



Figure 15: DDC filter gain Bode diagram

We can see that the resulting cutoff frequency is at 86 kHz, and that the stop-band attenuation is at - 100 dB (Figure 15).

If we take a closer look at the pass-band (*Figure 16*), we can see that the 0.1dB ripple requirement has been successfully met as well:



Figure 16: DDC filter pass-band ripple



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6 DDC FILTERING FREQUENCY RESPONSE VERIFICATION

To verify the frequency response of the previously designed filter, the EAGLE_16-125 narrow band will be fed with a 70 MHz signal on analog input n°0. This input will be mapped to processing units 0 to 4 so as to each processing unit can treat a specific frequency range, each by using a different demodulation frequency. The sampling frequency will be 100 MHz.

We are going to see how to configure the EAGLE_16-125 to do this verification.

6.1 EAGLE_16-125 configuration

6.1.1 Acquisition parameters

Acquisition parameters consist in several configurations:

- sampling clock frequency
- trigger edge and sampling clock (internal or external)
- trigger electrical standard

Sampling Frequency ?	10000000(Hz	Current: 100.000849 MHz	
Cantura mode a	Edge: capture at trigger RISING edge 🔹		
Capture mode :	Clock sar	npling source External clock 🔻	
Trigger electrical std. ?	LVPECL V		

Figure 17: Acquisition parameters

<u>Note:</u>

It is important to configure the sampling clock frequency **prior** to configure the DDS frequencies (*Figure 17*). Sampling clock frequency is used by EAGLE_16-125 to calculate the configuration value which will be applied to the DDS.

6.1.2 Filtering parameters

In order to verify the filter's frequency response in one recording session, the five processing units are used simultaneously. On the five processing units (PU [0...4]), central frequencies of the DDS are set to observe filter attenuation at different frequencies (Table 2).

PU number	foos (MHz)	Frequency obtained after demodulation (kHz)
0	69.95	50
1	69.914	86
2	69.9	100
3	69.885	115
4	69.8	200

Table 2: Central frequencies of the Processing Units' DDS





In order to have a descent FFT, widths of the windowing on the five processing units are set to get 10 000 I/Q complex output samples. We do not use any offset, and the period is set to infinity (*Figure 18*).

	Channel linked Channel 0	Central Frequency 69.95 MHz	Decimation 420	Peak Rate 1.905 MB/s	Data rate 0.08 MB/s
	Input Cł	nannel selection	Output Destination selection Interface 0 ; IP 0: 10.1.1.4 v ? Configure <u>network params.</u> first		
		Filtering s	election ed 💙 <u>?</u>		
		Input Win	dowing		
	Offset ?	0 samples	art Trigger event	Period	
	Width <u>?</u>	4200000 samples	Offset		
	Period <u>?</u> Set infinite	4294967 samples		Width	—
Manage PU#0	period	1	_!		→t
	DDS Ce 69.95	ntral Frequency	Filtering DI	DC Decimation	

Figure 18: Processing Unit (PU) number 0 configuration

Once the five processing units configured, user can check the filtering configuration on the summary configuration page (Figure 19).

Processing Unit#	Summary configuration				
Manage PU#0	Channel linked	Central Frequency	Decimation	Peak Rate	Data rate
	Channel 0	69.95 MHz	420	1.905 MB/s	0.08 MB/s
Manage PU#1	Channel linked	Central Frequency	Decimation	Peak Rate	Data rate
	Channel 0	69.914 MHz	420	1.905 MB/s	0.08 MB/s
Manage PU#2	Channel linked	Central Frequency	Decimation	Peak Rate	Data rate
	Channel 0	69.9 MHz	420	1.905 MB/s	0.08 MB/s
Manage PU#3	Channel linked	Central Frequency	Decimation	Peak Rate	Data rate
	Channel 0	69.885 MHz	420	1.905 MB/s	0.08 MB/s
Manage PU#4	Channel linked	Central Frequency	Decimation	Peak Rate	Data rate
	Channel 0	69.8 MHz	420	1.905 MB/s	0.08 MB/s

Figure 19: Configuration summary of the five processing units





6.1.3 Network configuration

Do not forget to configure the network in order to correctly receive EAGLE_16-125 data.

	IP and MAC Host Address					
		IP MAC	10 . 1 . 1 . 1 40:D8:55:16:B0:20			
IP Interface 0	Node#		IP/MAC Destination Addresses	Active	Delete	
	#1	IP MAC	10 1 1 4 00 : 00 : 00 : 00 : 00 : 00 : 00 00 : 00 00	V		
			Add new IP destination			

Figure 20: Network configuration

6.2 Processing results

With these configurations applied, the outputs of the five processing are recorded, the complex I/Q modulus spectrums calculated and plotted. Here are the results:



DDC results: fs= 100MHz; Decimation= 420; fc= 86kHz





Table 3 explains the Figure 21 results.

PU number	Plot color	Frequency (kHz)	Comments
0	Blue	50	Within filter's bandwidth
1	Red	86	Filter's cutoff frequency: -3dB from bandwidth gain
2	Green	100	Filter's roll-off
3	Cyan	115	Filter's stop-band -100 dB from bandwidth gain
4	Black	200	Completely attenuated: filter's attenuation goes stronger as frequency increases

The designed filter fits the specification's needs.







7 APPENDIX

7.1 cFIR filter coefficient set coe file

Here is the coe file used within this application note to configure the cFIR filter:

```
; Digital filter coefficients for Xilinx FIR implementation using Xilinx Coregen.
Radix = 10;
Coefficient_Width = 25;
CoefData = -6794,
-25628,
19066,
152768,
10996,
-520839,
-247891,
1309132,
1095492,
-2722296,
-3752867,
4976276,
16488836,
16488836,
4976276,
-3752867,
-2722296,
1095492,
1309132,
-247891,
-520839,
10996,
```

10996, 152768, 19066, -25628, -6794;

7.2 pFIR filter coefficient set coe file

Here is the coe file used within this application note to configure the pFIR filter:

```
; Digital filter coefficients for Xilinx FIR implementation using Xilinx Coregen.
Radix = 10;
Coefficient_Width = 25;
CoefData = 1394,
1382,
-8899,
-35117,
-60783,
-48806,
15695,
80723,
59080,
-59529,
-147405,
-59387,
154922,
```







233604,
2217,
-325120,
-303784,
170905,
573806,
284514,
-533130,
-874463,
-46518,
119/142,
1167994,
-/UZ378,
-2040626,
3682586
9865269
12653715
9865269
3682586.
-1388284,
-2545628,
-702598,
1169994,
1197142,
-46518,
-874463,
-533130,
284514,
5/3806,
170905,
-303/84,
-323120,
2217,
154922
-59387
-147405
-59.529
59080.
80723,
15695,
-48806,
-60783,
-35117,
-8899,
1382,
1394;

7.3 SNMP configuration script

#! /bin/sh

IP=\${1:-192.168.0.150}







echo "Sample frequency" snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.2.9.0 i 10000000 echo "Capture mode: Rising Edge trigger" snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.2.6.0 i 0 echo "Trigger Level: LVPECL" snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.2.7.0 i 1

echo "DDS frequencies"

snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.1.1.0 i 69950000 # 50kHz
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.2.1.0 i 69914000 # 86kHz
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.3.1.0 i 69900000 # 100kHz
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.4.1.0 i 69885000 # 115kHz
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.5.1.0 i 69800000 # 200kHz
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.6.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.7.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.8.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.9.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.10.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.11.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.12.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.13.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.14.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.15.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.16.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.17.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.18.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.19.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.20.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.21.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.22.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.23.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.24.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.25.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.26.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.27.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.28.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.29.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.30.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.31.1.0 i 15000000
snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.32.1.0 i 15000000

echo "Channel/PU mapping"

snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.1.6.0 i 0 # Analog input 0 snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.2.6.0 i 0 snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.3.6.0 i 0 snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.4.6.0 i 0 snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.5.6.0 i 0 snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.6.6.0 i 16 # 16 means OFF snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.7.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.7.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.7.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.7.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.7.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public $\{P\}$ 1.3.6.1.4.1.35959.1.2.3.7.6.0 i 16







snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.11.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.12.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.13.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.14.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.15.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.16.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.17.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.18.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.19.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.20.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.21.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.22.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.23.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.24.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.25.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.26.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.27.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.28.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.29.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.30.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.31.6.0 i 16 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.32.6.0 i 16 #Filters configuration echo" FILTERS CONFIGURATION" for pu num in (seg 1 1 5); do echo "DDC decimation setting" decimation=420 snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.\${pu_num}.7.0 i \${decimation} echo "cFIR coefficient loading" coefile=\$(cat ../COE/EAGLE_fs_100_MHz_Decim_420_fc_86_kHz_cFIR.coe) snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.\${pu_num}.8.0 s "\$coefile" echo "pFIR coefficient loading" coefile=\$(cat ../COE/EAGLE fs 100 MHz Decim 420 fc 86 kHz pFIR.coe) snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.\${pu_num}.9.0 s "\$coefile" done; # Windowing configuration echo" WINDOWING CONFIGURATION" for pu_num in \$(seq 1 1 5); do ###### BYPASS PARAMETER MUST BE SET BEFORE INPUT WINDOW ####### echo "BvPass" snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.\${pu_num}.3.0 i 0 echo "Input Window" snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.\${pu_num}.4.0 s "0 4200000 4294967295" echo "Output Destination" snmpset -v 1 -r 0 -t 15 -c public \${IP} 1.3.6.1.4.1.35959.1.2.3.\${pu_num}.5.0 s "0 0" done.

7.4 MATLAB design script

```
clc; close all; clear all;
%% System parameters
fs = 100e6; % Sampling frequency
User Decim = 420; % CIC decimation factor
```







```
User pFIR Fc
                 = 89.5e3; % Desired filter cutoff frequency
User_pFIR_Ripple = 0.1;
                             % Bandpass ripple
%% Targeted .coe files paths
 % cFIR's coe file write location
cFIR coe path = strcat('./coe/EAGLE fs ',...
                         num2str(fs/1e6),...
                          '_MHz_Decim_',...
                         num2str(User_Decim),...
                          ' fc_',...
                         num2str(User pFIR Fc/1e3),...
                          ' kHz cFIR.coe');
% pFIR's coe file write location
pFIR_coe_path = strcat('./coe/EAGLE_fs_',...
                         num2str(fs/1e6),...
                          ' MHz Decim ',...
                         num2str(User_Decim),...
                          ' fc_',...
                         num2str(User pFIR Fc/1e3),...
                          '_kHz_pFIR.coe');
%% CIC filter design
% Parameters
M_CIC = User_Decim/4; % Decimation factor
DD_CIC = 1; % Differential delay
                  % Differential delay
Nsecs_CIC = 5;
                          % Number of sections
% Design
hCIC = mfilt.cicdecim(M_CIC,DD_CIC,Nsecs_CIC);
% Frequency response with normalized magnitude
hfvt_cic = fvtool(hCIC, 'Fs', fs);
legend(hfvt_cic, 'CIC gain response');
set(hfvt_cic, 'Name', 'CIC Gain Bode diagram');
set(hfvt_cic, 'ShowReference', 'off')
set(hfvt_cic, 'NormalizeMagnitudeto1', 'on');
set(hfvt cic, 'FrequencyScale', 'Log');
%% CIC droop compensation FIR (cFIR) design
% Parameters
              = 2;
                                  % Decimation factor
M cFIR
cFIR_fcutoff = (fs/M_CIC)/4; % Cutoff frequency @ 6dB (max.: (fs/M_CIC)/4 to avoid
aliasing)
             = 25;
                                  % Filter order
cFIR Order
                                  % Pass band maximum ripple
             = 0.001;
Apass
             = 100;
                                  % Stop band attenuation
Astop
% Design
cFIR_Specs = fdesign.decimator(M_cFIR, 'ciccomp', DD_CIC, Nsecs_CIC,...
                                  'n,fc,ap,ast', cFIR_Order, cFIR_fcutoff,...
Apass, Astop, fs/M_CIC);
hcFIR = design(cFIR_Specs, 'equiripple');
% Frequency response with normalized magnitude
hfvt_cfir = fvtool(hcFIR, 'Fs', fs/M_CIC);
set(hfvt cfir, 'FrequencyScale', 'Log');
legend('cFIR gain response');
% Gain normalization for comparison with the cFIR filter
hNorm Gain = dfilt.scalar(1/gain(hCIC));
hCIC_norm = cascade(hNorm_Gain,hCIC);
```







```
% Displaying CIC & cFIR combined response
hfvt_cic_cfir = fvtool(hCIC_norm, hcFIR, cascade(hCIC_norm, hcFIR), 'Fs', [fs
fs/M CIC fs]);
legend(hfvt_cic_cfir, 'CIC', 'cFIR', 'CIC + cFIR');
set(hfvt_cic_cfir, 'ShowReference', 'off')
set(hfvt_cic_cfir, 'NumberofPoints', 65536)
set(hfvt_cic_cfir, 'FrequencyRange', 'Specify freq. vector');
fvector = linspace(0, 4e6, 65536);
set(hfvt cic cfir, 'FrequencyVector', fvector);
axes = get(hfvt cic cfir, 'CurrentAxes');
set(axes, 'YLim', [-250 10]);
% Displaying cFIR's compensation effect on CIC's droop
hfvt droop = fvtool(hCIC norm, hcFIR, cascade(hCIC norm, hcFIR), 'Fs', [fs fs/M CIC
fs]);
legend(hfvt_droop, 'CIC''s droop', 'CFIR''s compensation', 'CIC''s compensated
droop');
set(hfvt_droop, 'ShowReference', 'off')
set(hfvt_droop, 'NumberofPoints', 65536)
set(hfvt_droop, 'FrequencyRange', 'Specify freq. vector');
fvector = linspace(0, User_pFIR_Fc, 65536);
set(hfvt droop, 'FrequencyVector', fvector);
%% pFIR design
% Parameters
M_pFIR = 2;
                                 % Decimation factor
pFIR Fc
          = User_pFIR_Fc;
                                 % Cutoff frequency @ 6dB
                                 % Filter order
pFIR_Order = 60;
Apass = User_pFIR_Ripple; % Pass band maximum ripple
Astop
          = 100;
                                 % Stop band attenuation
% Design
pFIR_Specs = fdesign.decimator(M_pFIR, 'Lowpass', 'n, fc, ap, ast', pFIR Order,
pFIR_Fc, Apass, Astop, fs/(M_CIC*M_cFIR));
hpFIR = design(pFIR Specs, 'equiripple');
% Frequency response with normalized magnitude
hfvt pfir = fvtool(hpFIR, 'Fs', fs/(M CIC*M cFIR));
% set(hfvt pfir, 'FrequencyScale', 'Log');
legend('pFIR gain response');
%% Bit-true modelisation of the complete filter with truncatures
% Successive filters' bit widths
BitCicIn = 17; % CIC input width
                = 17; % cFIR input width
BitcFirIn
              = 24; % pFIR input width
BitpFirIn
BitpFirTruncOut = 32; % pFIR output width
% CIC Bit-true modelisation with truncatures
% CIC filter design
set(hCIC, 'InputWordLength',BitCicIn,...
          'InputFracLength',0,...
          'FilterInternals', 'FullPrecision');
BitCicOut = hCIC.OutputWordLength;
% Fixed-point CIC output truncature
hCICtrunc = dfilt.scalar(2^(BitcFirIn-BitCicOut));
set(hCICtrunc, 'Arithmetic', 'fixed',...
               'InputWordLength', BitCicOut,...
                'InputFracLength', 0,...
                'OutputMode', 'SpecifyPrecision',...
```







```
'OutputWordLength', BitcFirIn,...
                'OutputFracLength', 0,...
                'RoundMode', 'round');
%% cFIR Bit-true modelisation
% cFIR parameters
BitcFirCoef = 25;
% cFIR filter design
set(hcFIR, 'Arithmetic', 'fixed',...
            'InputWordLength', BitcFirIn,...
            'InputFracLength', 0,...
            'CoeffWordLength', BitcFirCoef,...
'FilterInternals', 'FullPrecision');
% Filter's coefficients written to .coe file
set2int(hcFIR, 25);
FirCoef2XilinxCoe(hcFIR.Numerator, 25, cFIR_coe_path);
% cFIR truncature
BitcFirOut = hcFIR.OutputWordLength;
BitcFirFracOut = hcFIR.OutputFracLength;
hcFIRtrunc = dfilt.scalar(2^(BitpFirIn-(BitcFirOut-BitcFirFracOut)));
set(hcFIRtrunc, 'Arithmetic', 'fixed',...
                 'InputWordLength', BitcFirOut,...
                 'InputFracLength', BitcFirFracOut,...
                 'OutputMode', 'SpecifyPrecision',...
                 'OutputWordLength', BitpFirIn,...
                  'OutputFracLength', 0,...
                 'RoundMode', 'round');
%% pFIR Bit-true modelisation
% pFIR parameters
BitpFirCoef = 25;
% pFIR filter design
set(hpFIR, 'Arithmetic', 'fixed',...
            'InputWordLength', BitpFirIn,...
            'InputFracLength', 0,...
'CoeffWordLength', BitpFirCoef,...
'FilterInternals', 'FullPrecision');
% Filter's coefficients written to .coe file
set2int(hpFIR, 25);
FirCoef2XilinxCoe(hpFIR.Numerator, 25, pFIR coe path);
% pFIR truncature
BitpFirOut
                 = hpFIR.OutputWordLength;
BitpFirFracOut = hpFIR.OutputFracLength;
hpFIRtrunc = dfilt.scalar(2^(BitpFirTruncOut-(BitpFirOut-BitpFirFracOut)));
set(hpFIRtrunc, 'Arithmetic', 'fixed',...
                 'InputWordLength', BitpFirOut,...
                  'InputFracLength', hpFIR.OutputFracLength,...
                  'OutputMode', 'SpecifyPrecision',...
                  'OutputWordLength', BitpFirTruncOut,...
                  'OutputFracLength', 0,...
                 'RoundMode', 'round');
%% Fixed-point complete FM filter
```

```
% FM filter static gain calculation
```







```
% CIC gain
CIC_static_gain = 20*log10(gain(hCIC));
% CIC truncature gain
CIC_trunc_gain = 20*log10(2^(hCICtrunc.OutputWordLength-
hCICtrunc.OutputFracLength)/2^(hCIC.OutputWordLength-hCIC.OutputFracLength));
% cFIR gain
f = linspace(0, 38e3, 10);
cFIR gain = 20*log10(abs(freqz(hcFIR, [0 2*pi*f/fs])));
cFIR static gain = cFIR gain(1);
% cFIR truncature gain
cFIR_trunc_gain = 20*log10(2^(hcFIRtrunc.OutputWordLength-
hcFIRtrunc.OutputFracLength)/2^(hcFIR.OutputWordLength-hcFIR.OutputFracLength));
% pFIR gain
f = linspace(0,10e3,1e6);
pFIR_gain = 20*log10(abs(freqz(hpFIR, [0 2*pi*f/fs])));
pFIR_static_gain = mean(pFIR_gain(2:end));
% pFIR truncature gain
pFIR trunc gain = 20*log10(2^(hpFIRtrunc.OutputWordLength-
hpFIRtrunc.OutputFracLength)/2^(hpFIR.OutputWordLength-hpFIR.OutputFracLength));
% FM filter static gain calculation
FM_filter_static_gain = CIC_static_gain + CIC_trunc_gain +...
                           cFIR_static_gain + cFIR_trunc_gain +...
                           pFIR static gain + pFIR trunc gain;
hFM_fi_Filter = cascade(hCIC, hCICtrunc, hcFIR, hcFIRtrunc, hpFIR, hpFIRtrunc);
hfvt_fm_filter = fvtool(hFM_fi_Filter, 'Fs', fs);
set(hfvt_fm_filter, 'NumberofPoints', 262144)
set(hfvt_fm_filter, 'NormalizeMagnitudeto1', 'on');
hfvt_fm_filter = fvtool(hFM_fi_Filter, 'Fs', fs);
legend(hfvt_fm_filter, 'Complete filter gain response');
set(hfvt_fm_filter, 'ShowReference', 'off')
set(hfvt_fm_filter, 'NumberofPoints', 262144)
set(hfvt_fm_filter, 'FrequencyRange', 'Specify freq. vector');
fvector = linspace(0, 2e6, 262144);
set(hfvt_fm_filter, 'FrequencyVector', fvector);
set(hfvt_fm_filter, 'NormalizeMagnitudeto1', 'on');
axes = get(hfvt fm filter, 'CurrentAxes');
set(hfvt fm filter, 'FrequencyScale', 'Log');
set(axes, 'YLim', [-250 10]);
```







8 SUPPORT INFORMATION

Should you have any questions or support requests, please feel free to contact TECHWAY.

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